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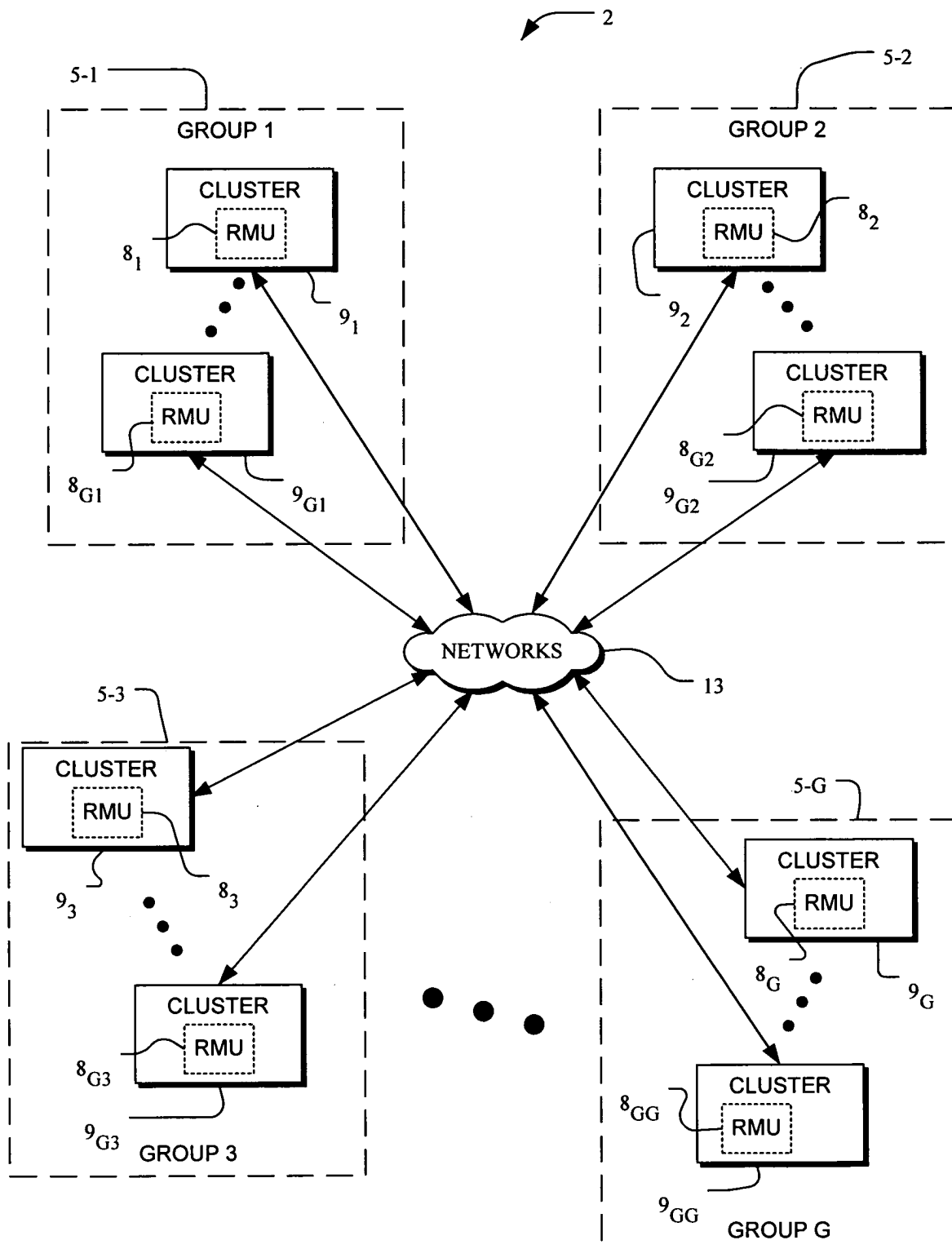
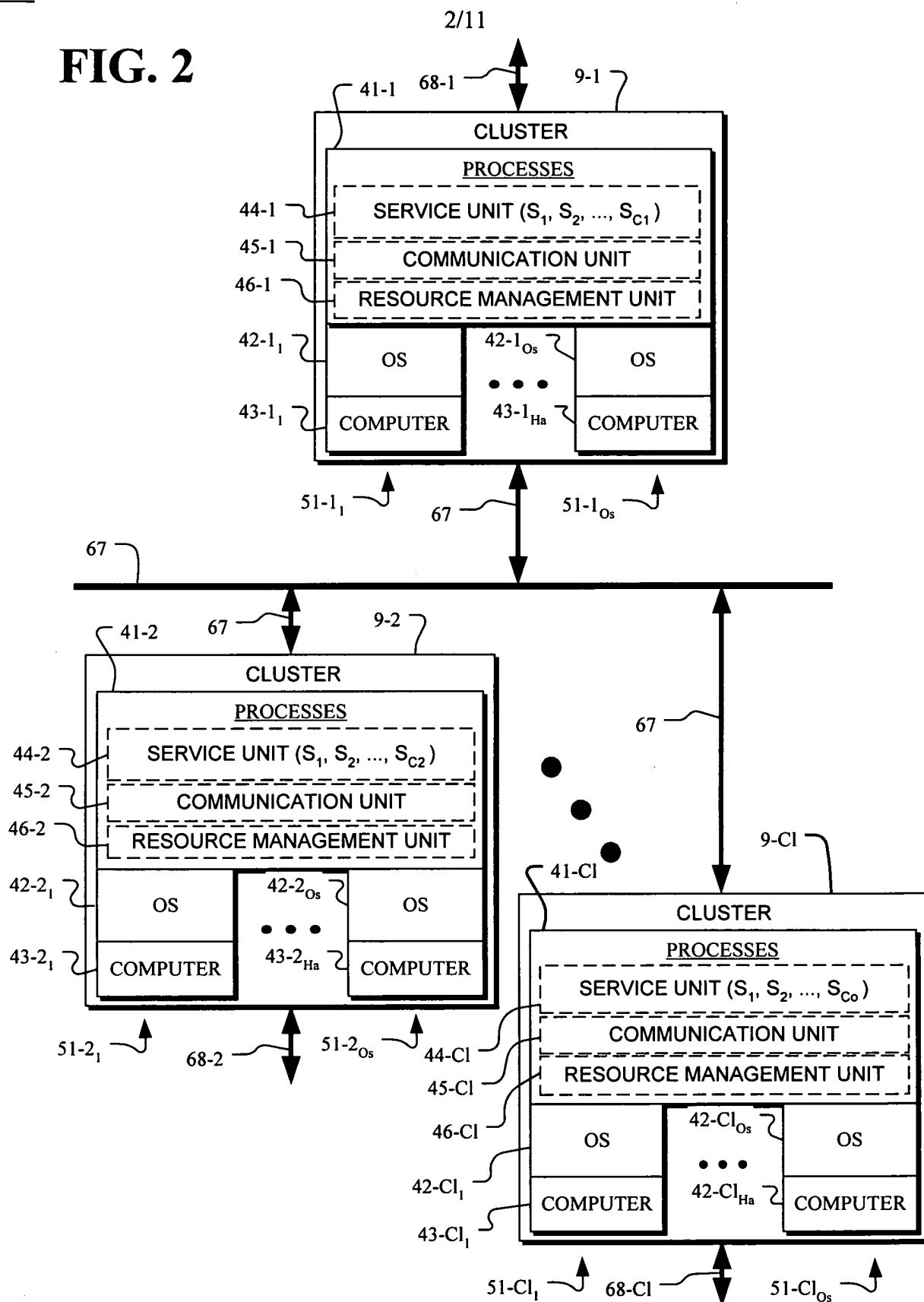
FIG. 1**6718486**

FIG. 2

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FIG. 3

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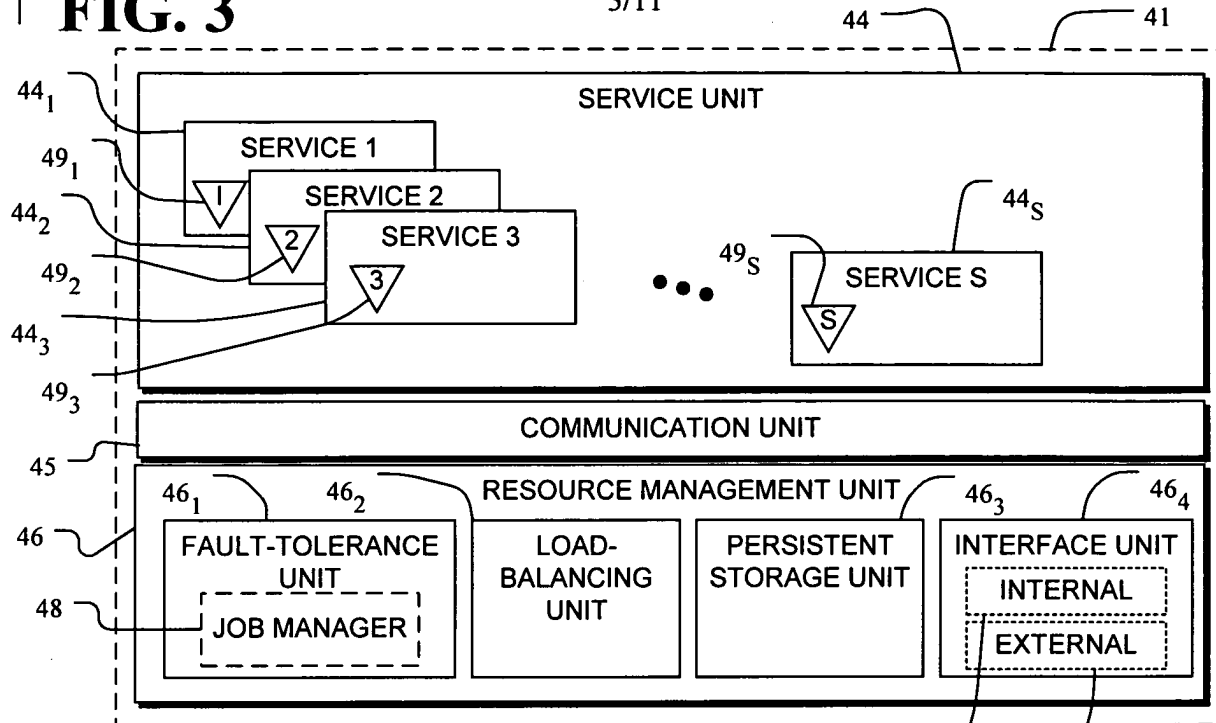
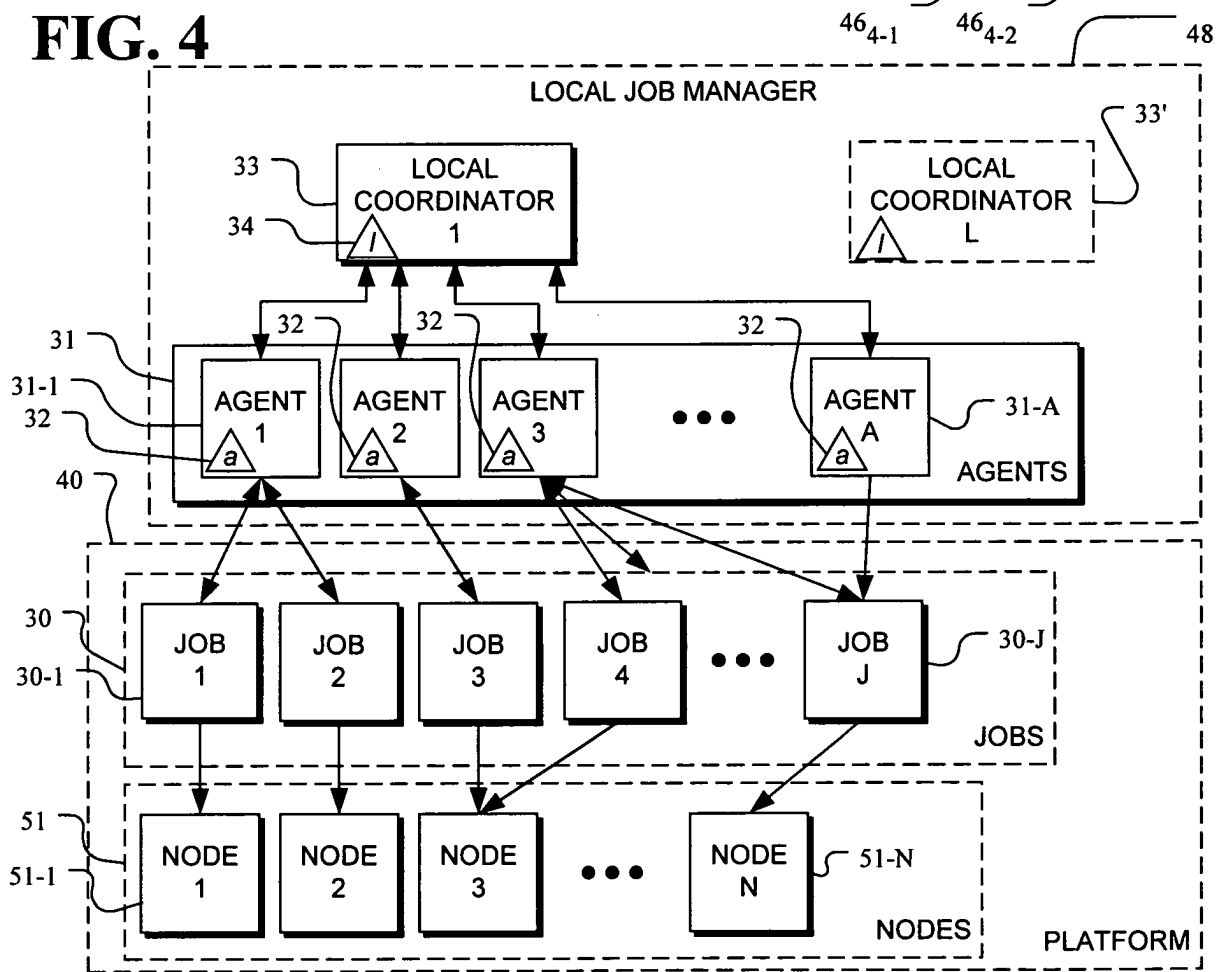
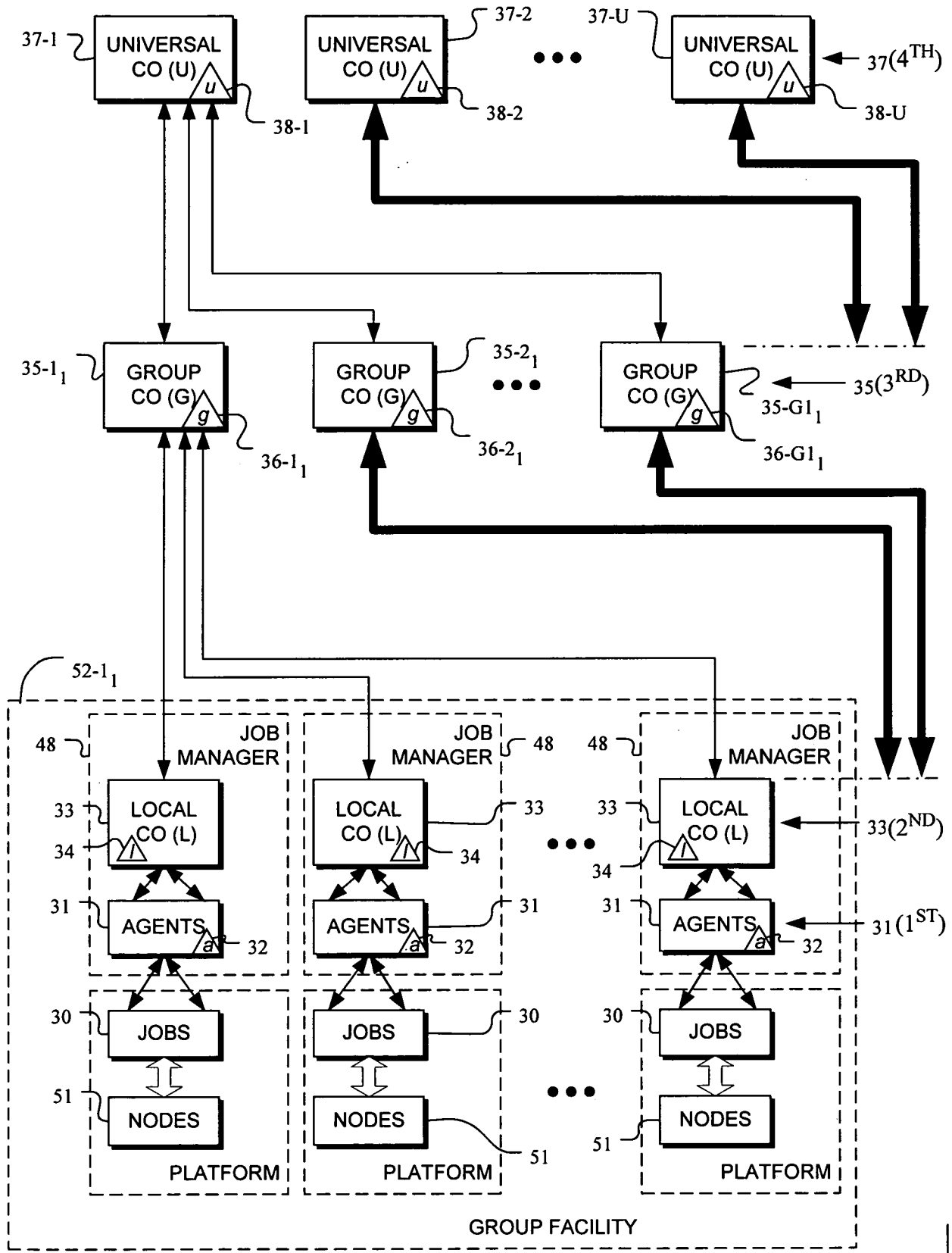
**FIG. 4**

FIG. 5

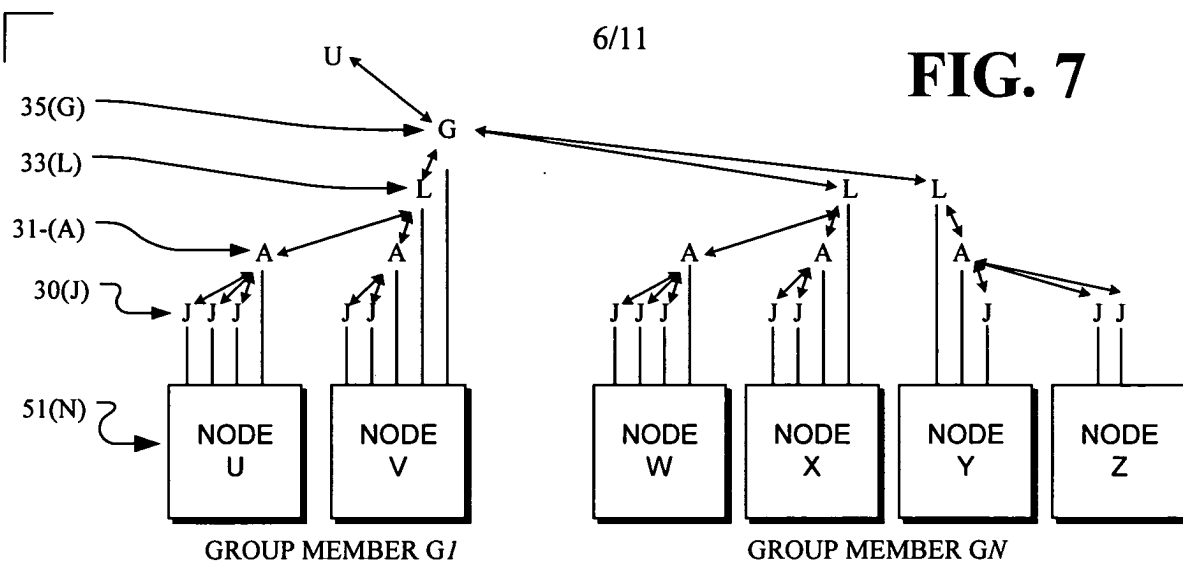
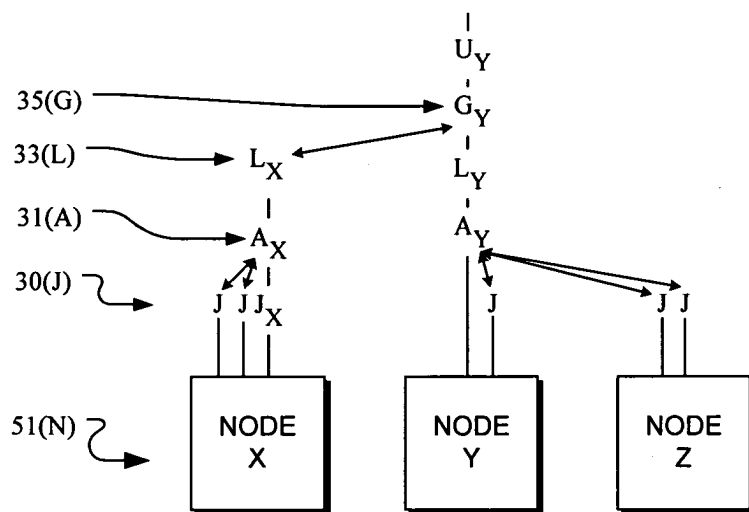
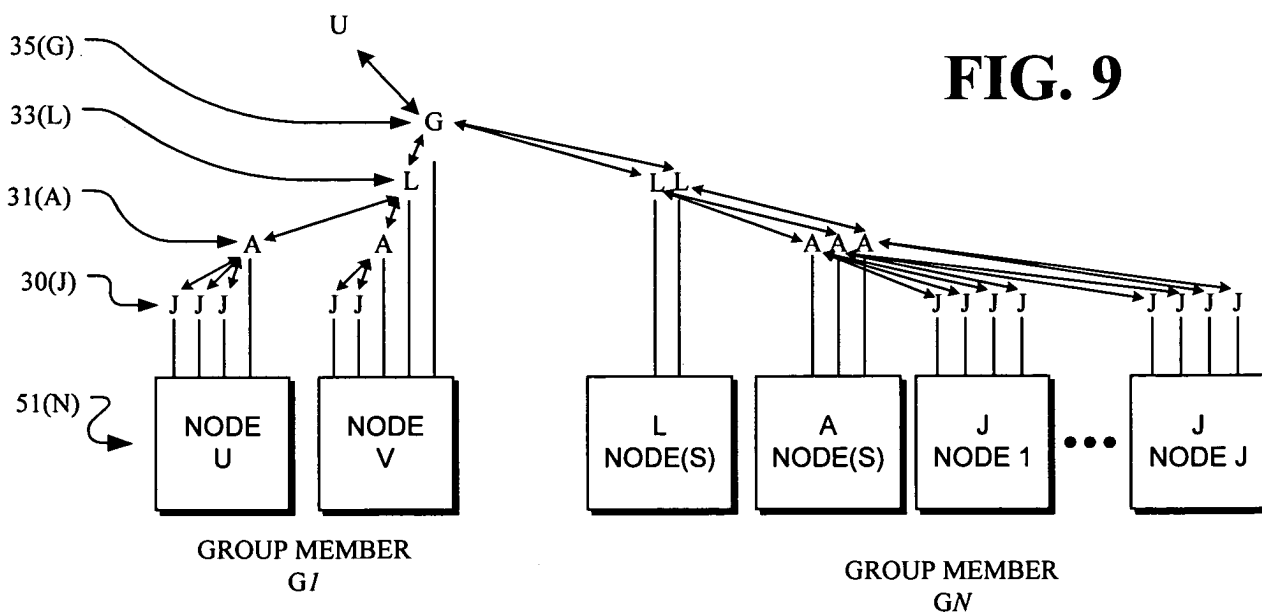
Title: FAULT-TOLERANCE FRAMEWORK FOR AN EXTENDABLE COMPUTER ARCHITECTURE

FIG. 6

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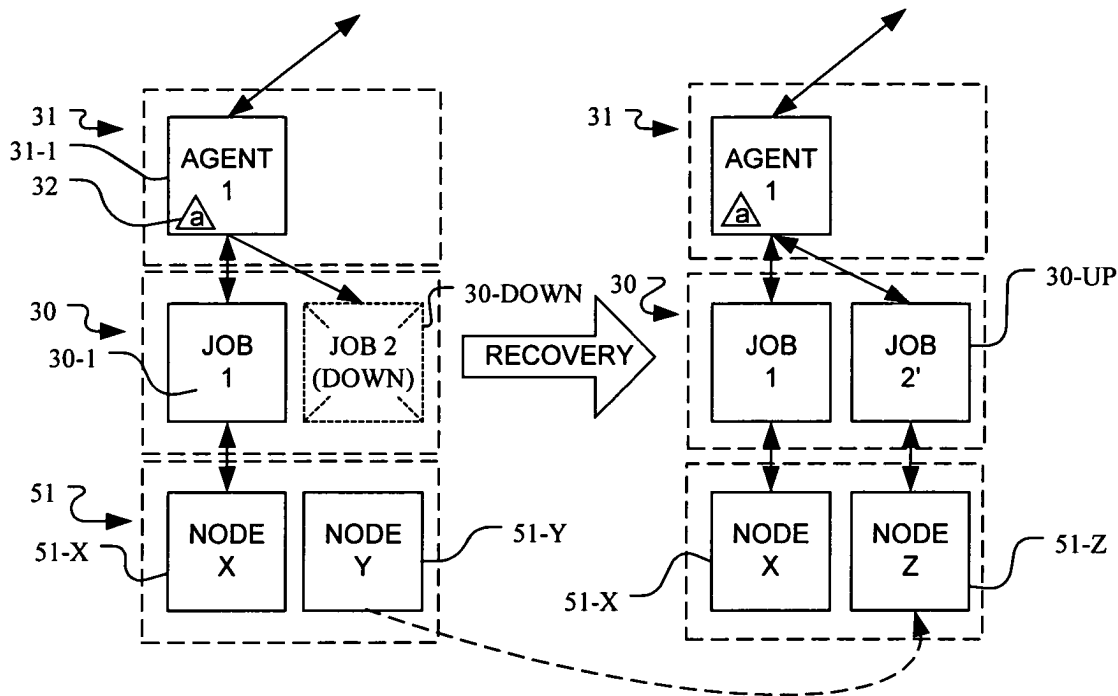
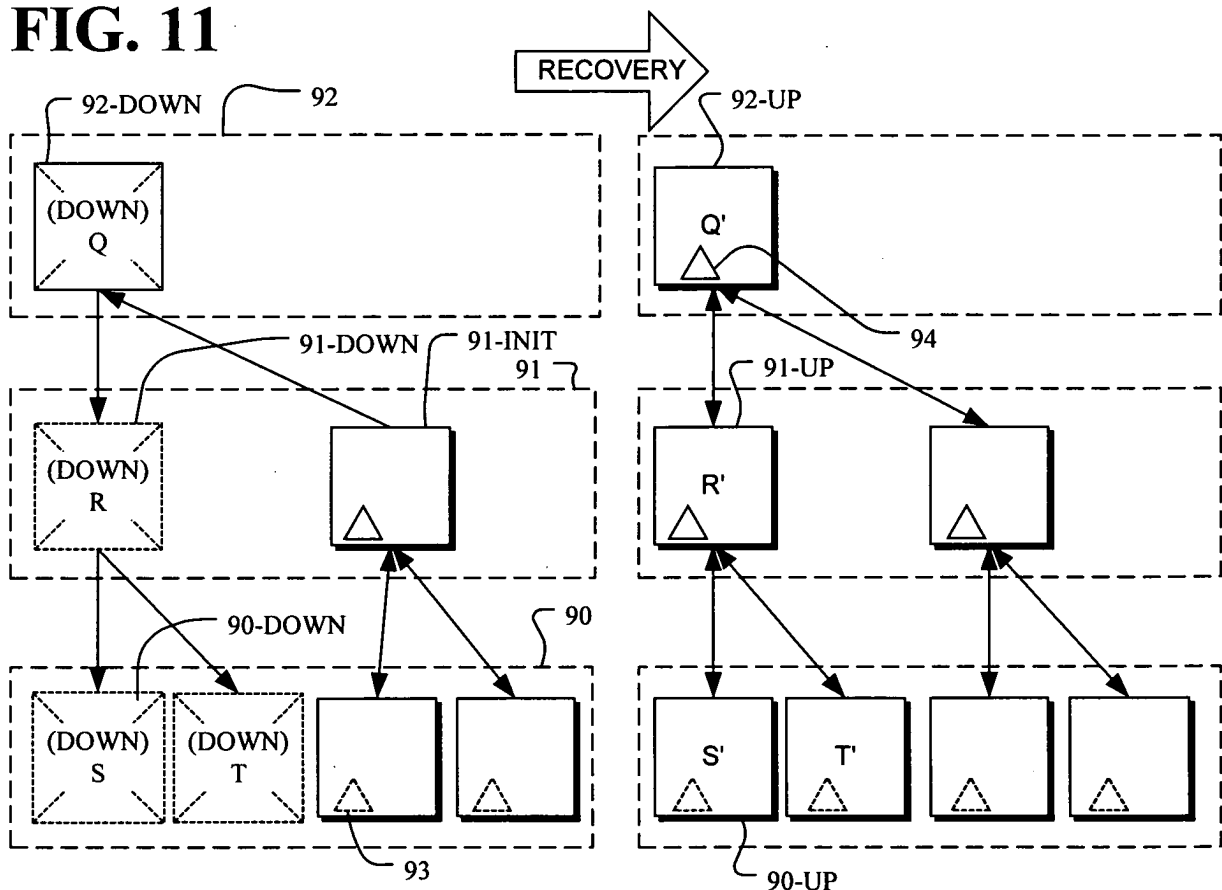
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FIG. 7**FIG. 8****FIG. 9**

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FIG. 10

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**FIG. 11**

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FIG. 12

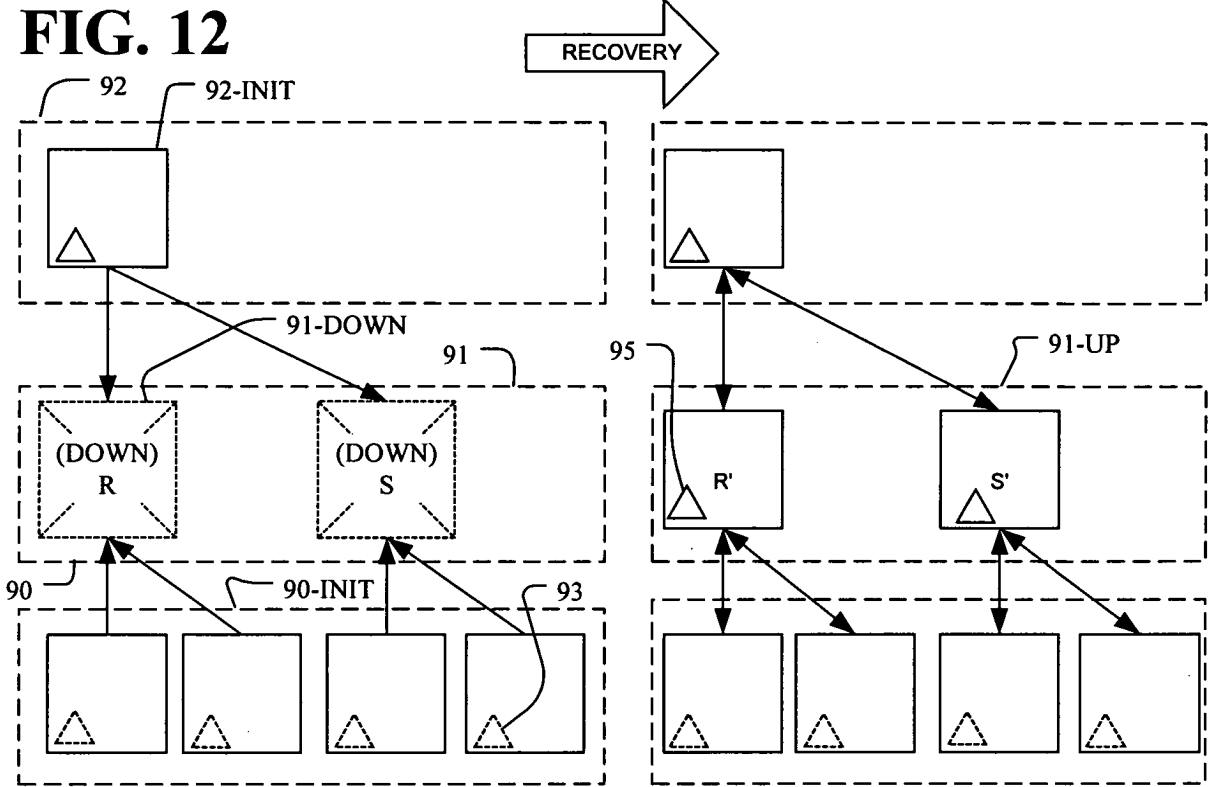
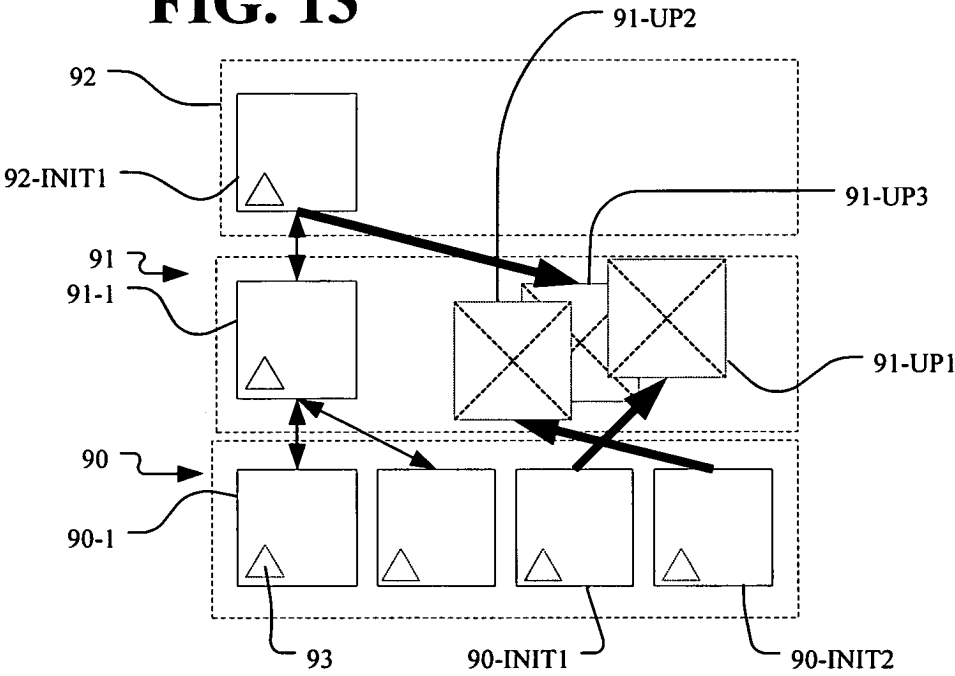


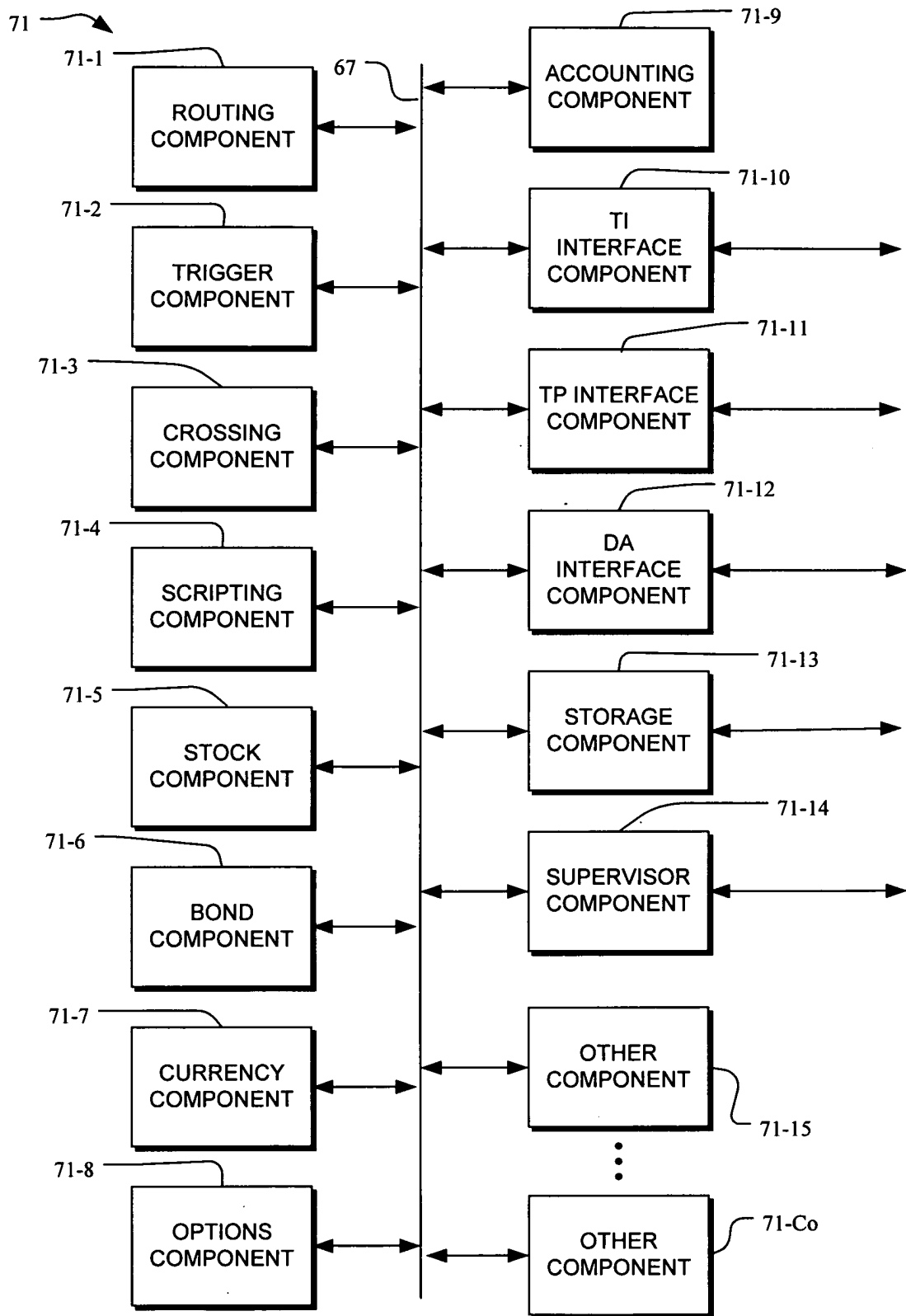
FIG. 13



Title: FAULT-TOLERANCE FRAMEWORK FOR AN EXTENDABLE COMPUTER ARCHITECTURE

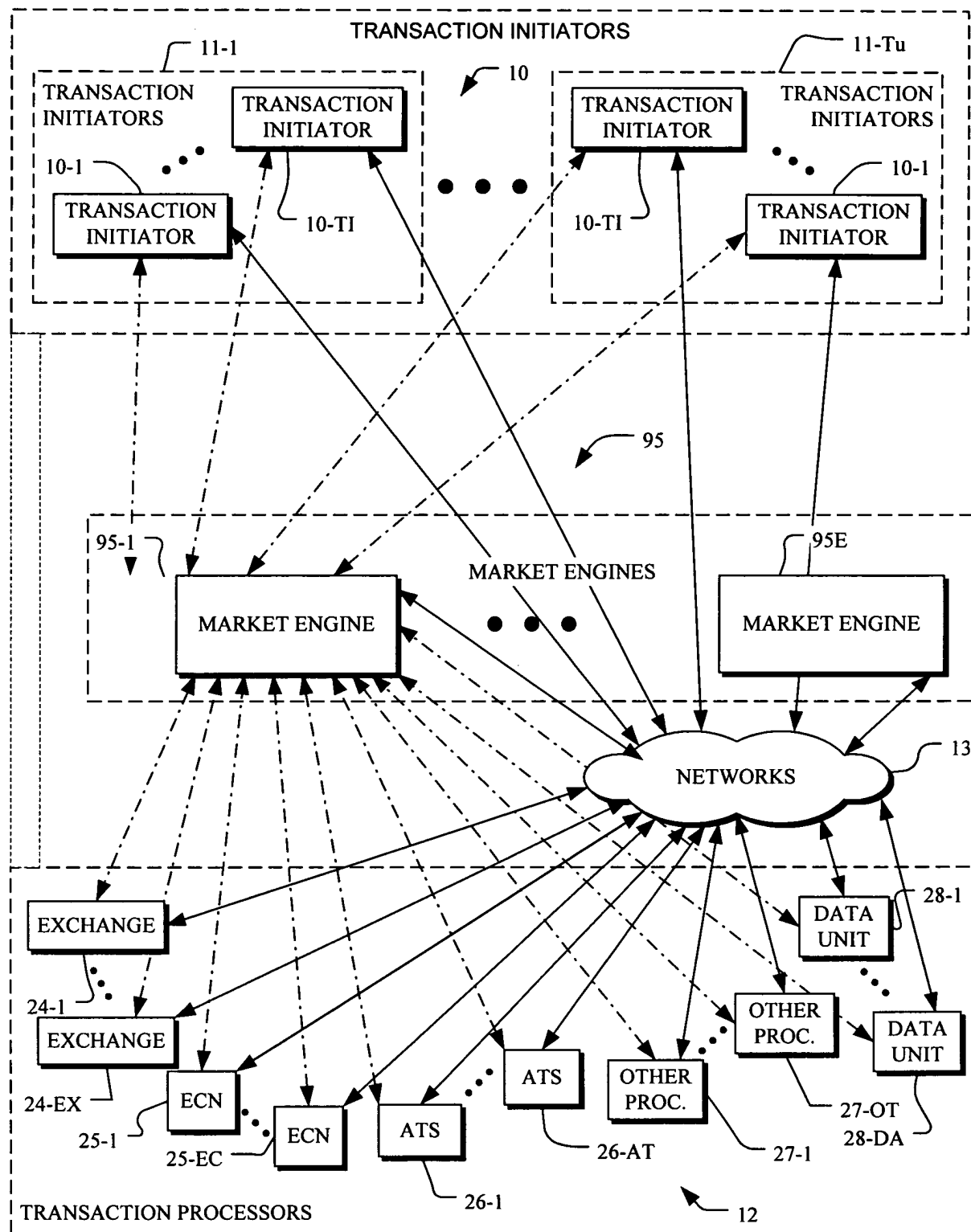
FIG. 14

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FIG. 15

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FIG. 16